

Attorney's Docket No.: A 17-031001 / S21-119638M/HW

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Shimizu et al.                      Art Unit : 2814  
Serial No. : 09/536898                                      Examiner : Louie, Wai Sing  
Filed : March 28, 2000  
Title : HYBRID INTEGRATED CIRCUIT DEVICE

Commissioner for Patents  
Washington, D.C. 20231

RESPONSE

In response to the action mailed September 24, 2002, please amend the application as follows:

In the claims:

Please amend claims 1 and 15 as follows:

- 1. (Twice Amended) A hybrid integrated circuit device comprising:
- a hybrid integrated circuit substrate in which at least a surface is provided with insulation;
  - at least a first electrode and at least a second electrode formed on said surface and being disposed to cover substantially the substrate;
  - a light emitting element connected with the first and second electrodes, said first and second electrodes configured to reflect light;
  - a seal which is disposed in a periphery of said substrate; and
  - a transparent substrate which is fixed to said hybrid integrated circuit substrate via said seal to enclose the first and second electrodes and the light emitting element within a sealed space formed between said hybrid integrated circuit substrate and said transparent substrate.

## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

December 24, 2002

Date of Deposit

Signature

Rose Papetti

Typed or Printed Name of Person Signing Certificate

Attorney's Docket No.: 10417-031001  
Client's Ref. No.: S21-119638M/HW

**OFFICIAL COMMUNICATION**

**FACSIMILE**

**FOR THE PERSONAL ATTENTION OF:**

**EXAMINER WAI SING LOUIE**

**U.S. PATENT & TRADEMARK OFFICE**

**GROUP 2814 FAX NO: (703)-308-7722 746-3939**

Number of pages including this page 12

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Examiner : Wai Sing Louie

FACSIMILE COMMUNICATION

Title : Hybrid Integrated Circuit Device

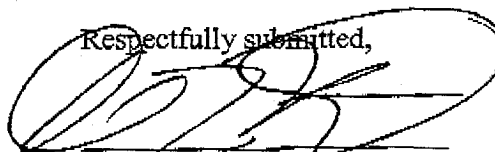
Commissioner for Patents  
P.O. Box 1450  
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OFFICIAL

Sir:

Attached to this facsimile communication cover sheet are copies of the following documents that were filed with the USPTO on December 24, 2002. Transmittal Letter, Response to Office Action, faxed this 5<sup>th</sup> day of August 2003, to Group 2814, the United States Patent and Trademark Office. Also attached is a copy of the postcard stamped by the USPTO on December 30, 2002:

Date: August 5, 2003


Respectfully submitted,  
  
Chris T. Mizumoto  
Reg. No. 42,899

Fish & Richardson P.C.  
45 Rockefeller Plaza, Suite 2800  
New York, New York 10111  
Telephone: (212) 765-5070  
Fax: (212) 258-2291

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Attorney's Docket No. 10417-031001	Express Mail Label No.	Mailing Date December 24, 2002	<b>For PTO Use Only</b> <i>Do Not Mark in This Area</i>
Application No. 09/536898	Filing Date March 28, 2000	Attorney/Secretary Init CTM/rap	
Title of the Invention HYBRID INTEGRATED CIRCUIT DEVICE			
Applicant Hisashi Shimizu et al.			
Enclosures • Transmittal Letter (1 pages) • Amendment/Response (9 pages) • Check in the amount of \$18.00			



Attorney's Docket No.: 1 .7-031001 / S21-119638M/HW

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Shimizu et al.      Art Unit : 2875  
Serial No. : 09/536898      Examiner : Unknown  
Filed : March 28, 2000  
Title : HYBRID INTEGRATED CIRCUIT DEVICE

Commissioner for Patents  
Washington, D.C. 20231

TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	21	-	20	=	1	\$18
Independent	3	-	3	=	0	\$0
First Presentation of Multiple Dependent Claims						\$0
						<u>\$0</u>
TOTAL FEE DUE						\$18

A check for \$18 is attached.

Respectfully submitted,



Chris T. Mizumoto  
Reg. No. 42,899

Date: December 24, 2002

Fish & Richardson P.C.  
45 Rockefeller Plaza, Suite 2800  
New York, New York 10111  
Telephone: (212) 765-5070  
Facsimile: (212) 258-2291

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